

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit includes a block having a first border edge on which an external connection terminal is provided and a
5 second border edge on which no external connection terminal is provided, a wiring prohibited area which extends a first distance from the first border edge and in which no wiring line running parallel to the first border edge exists, and a shielding line which
10 is at a second distance from the second border edge and runs parallel to the second border edge.